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FIG. 1

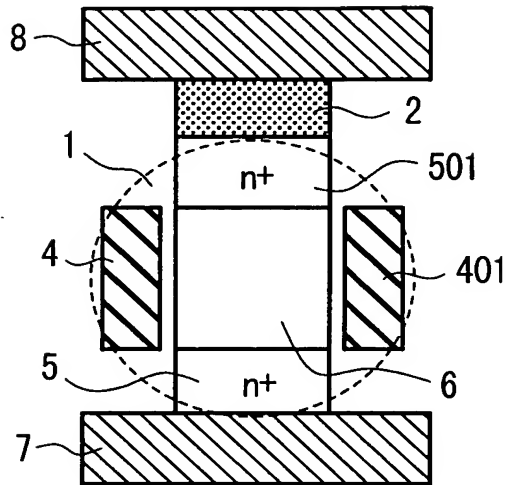


FIG. 2

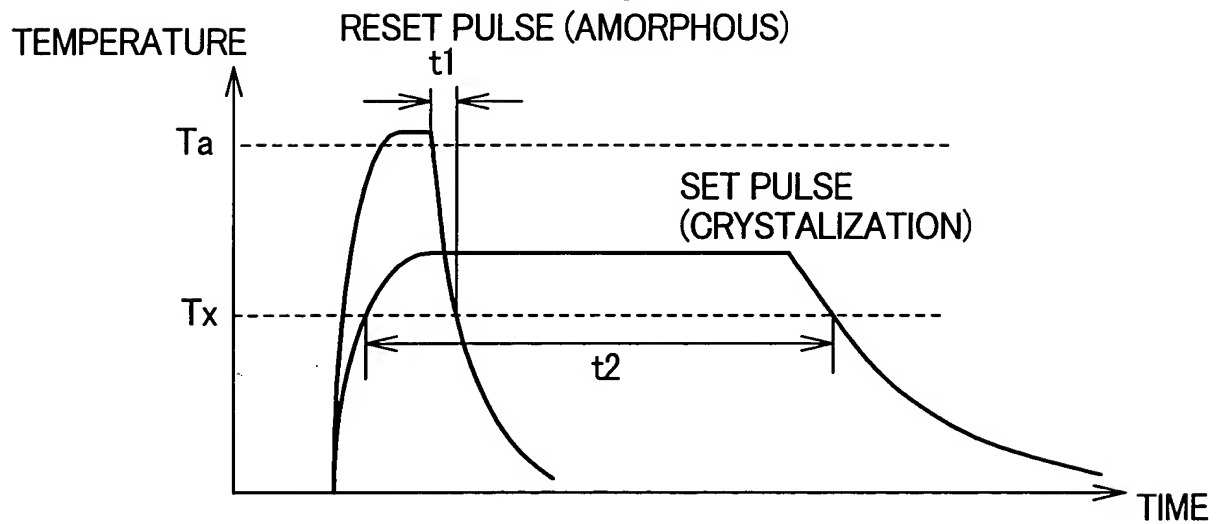


FIG. 3

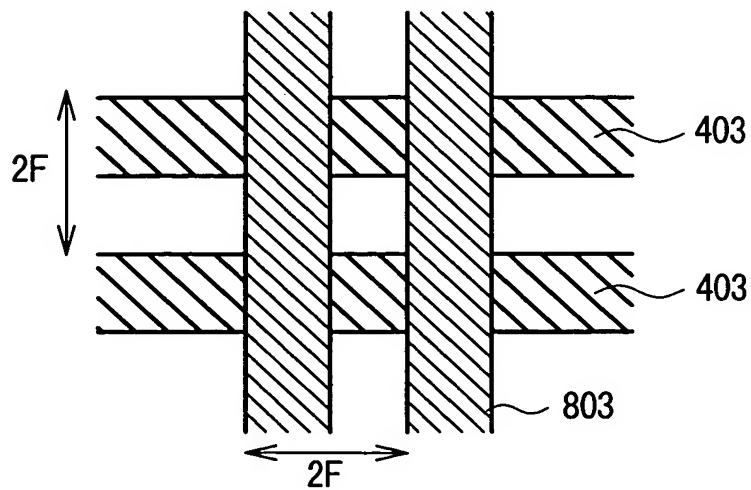


FIG. 4

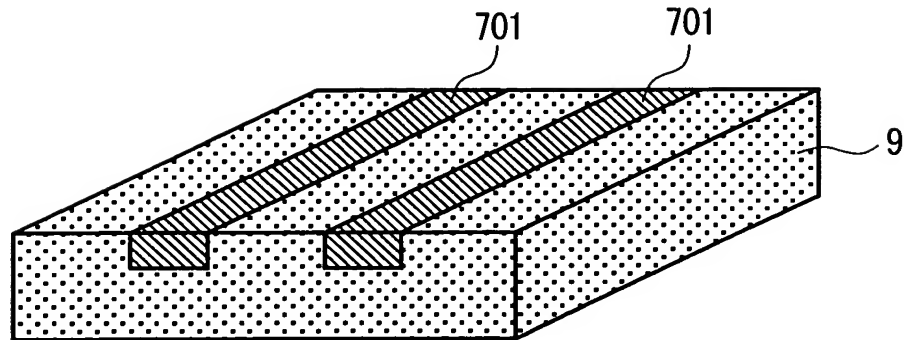


FIG. 5

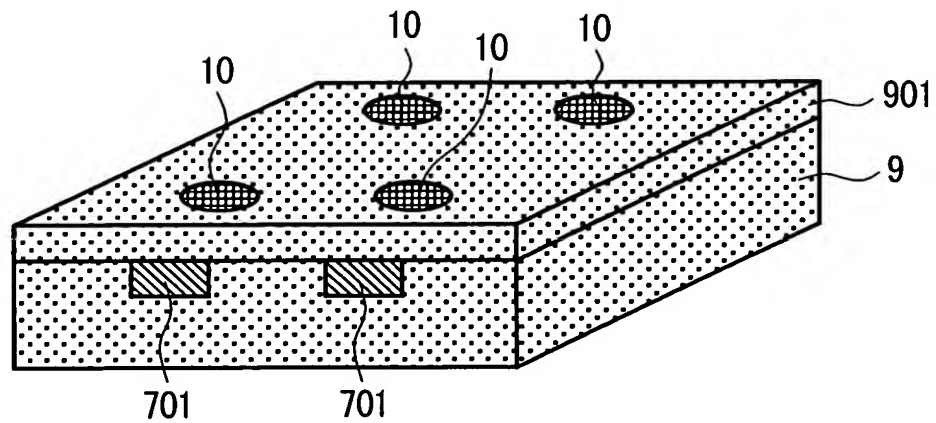


FIG. 6

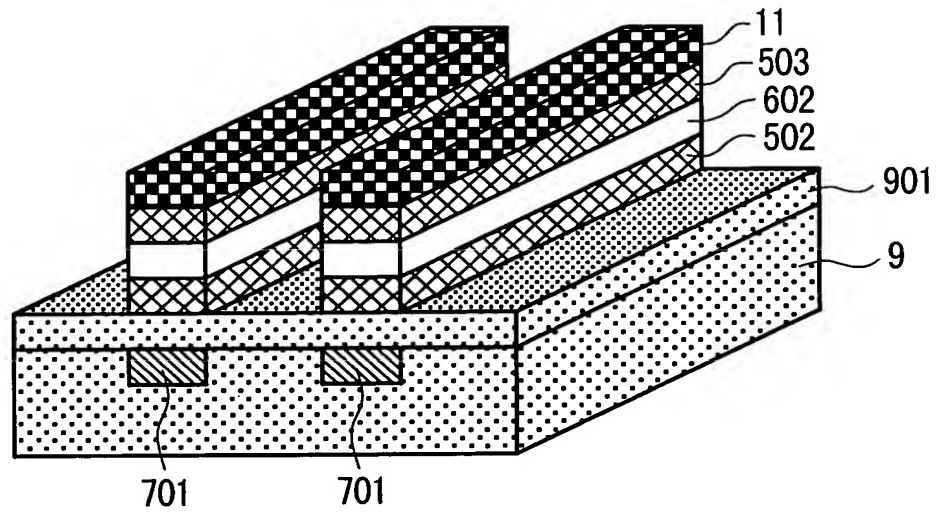


FIG. 7

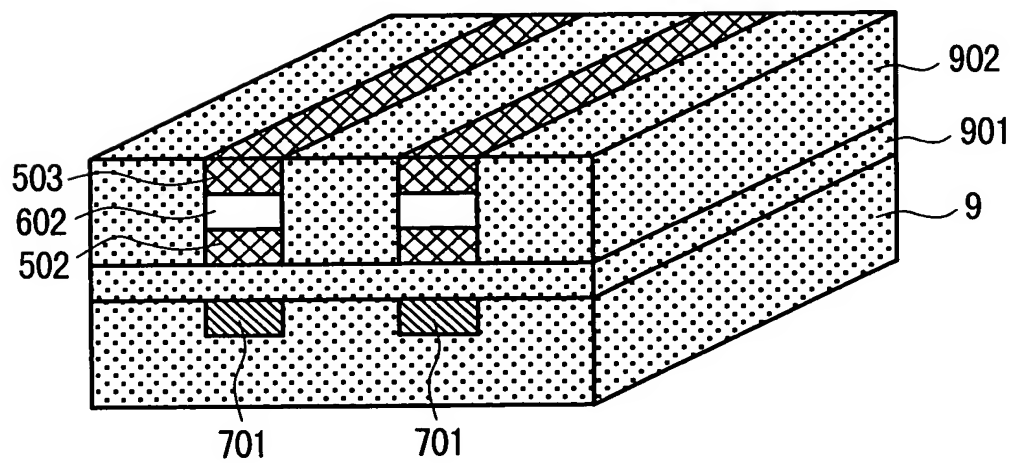


FIG. 8

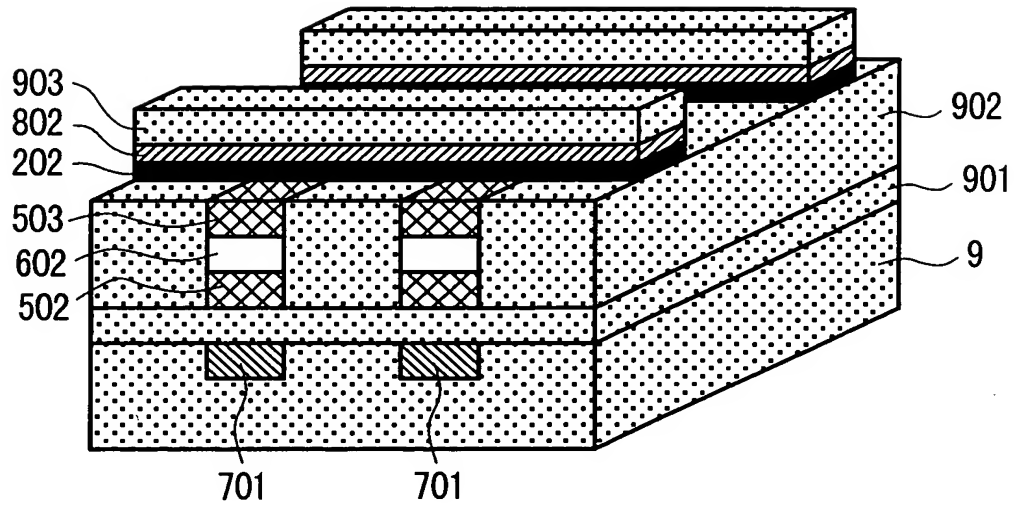


FIG. 9

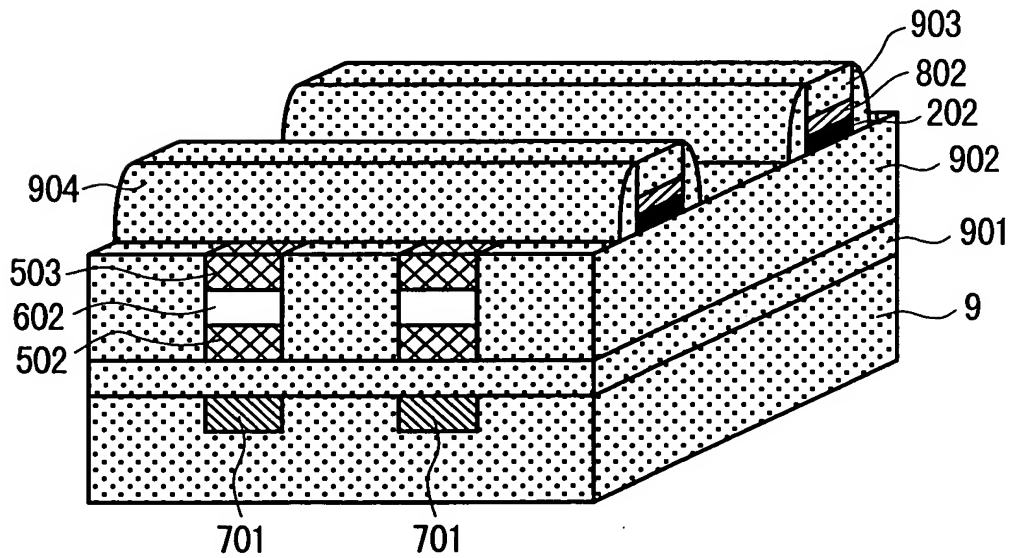


FIG. 10

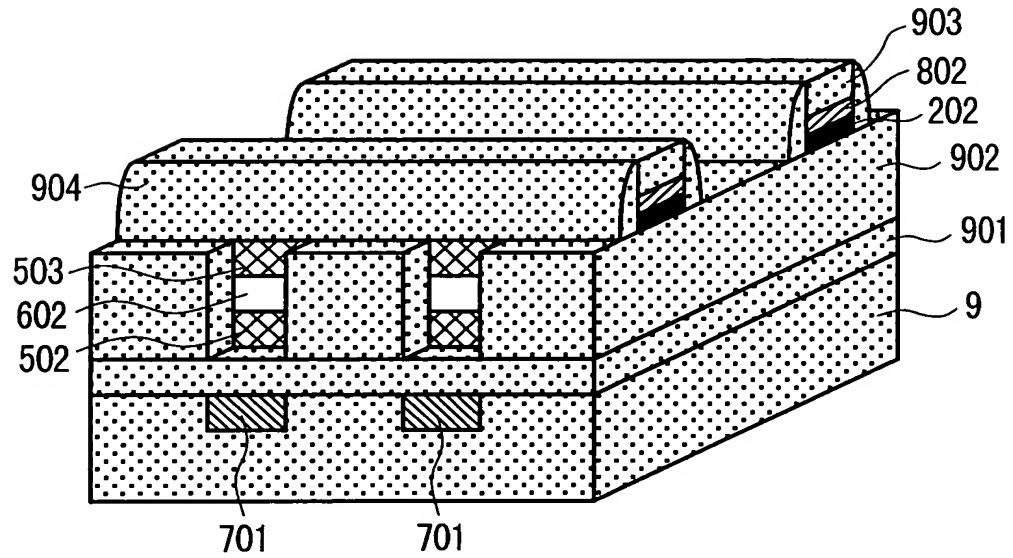


FIG. 11

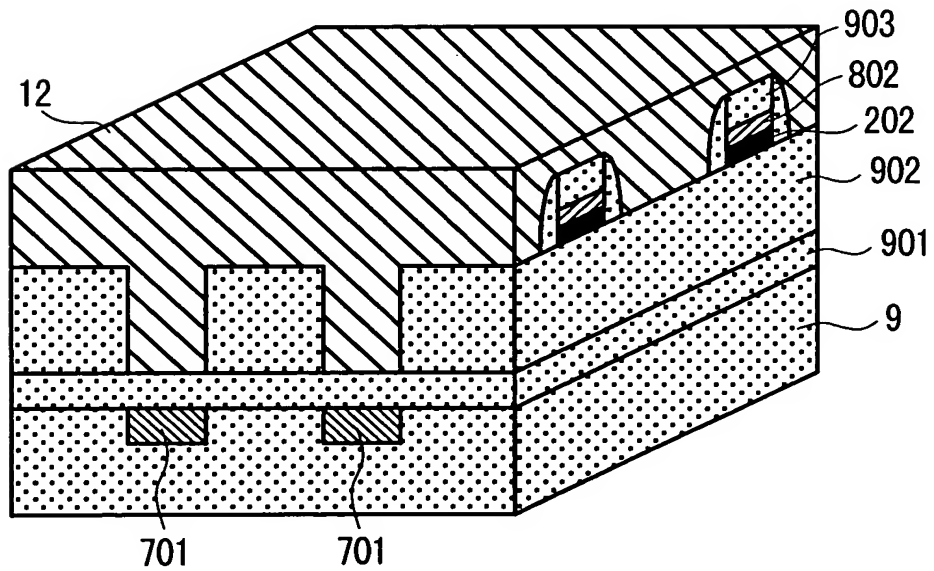


FIG. 12

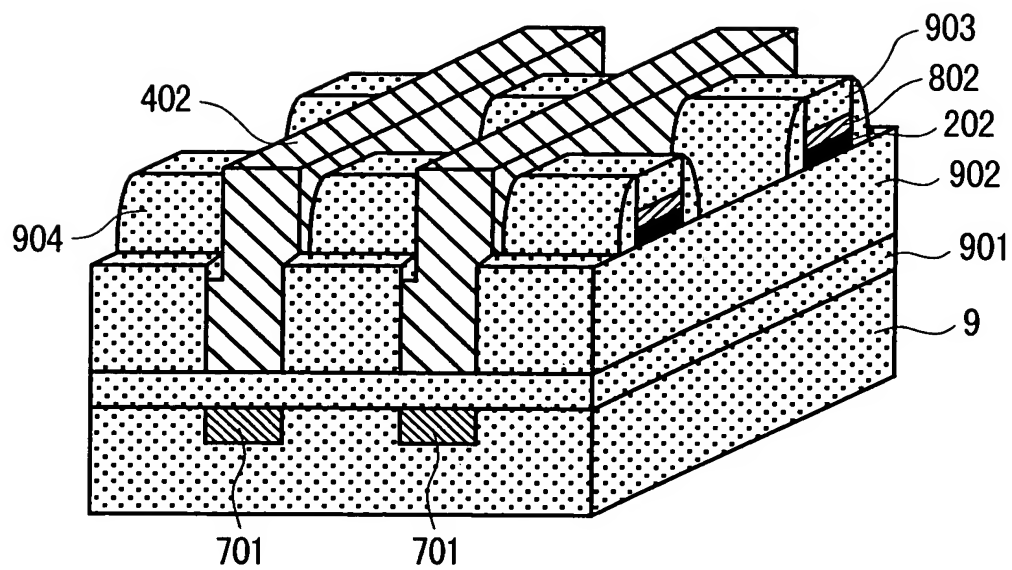


FIG. 13

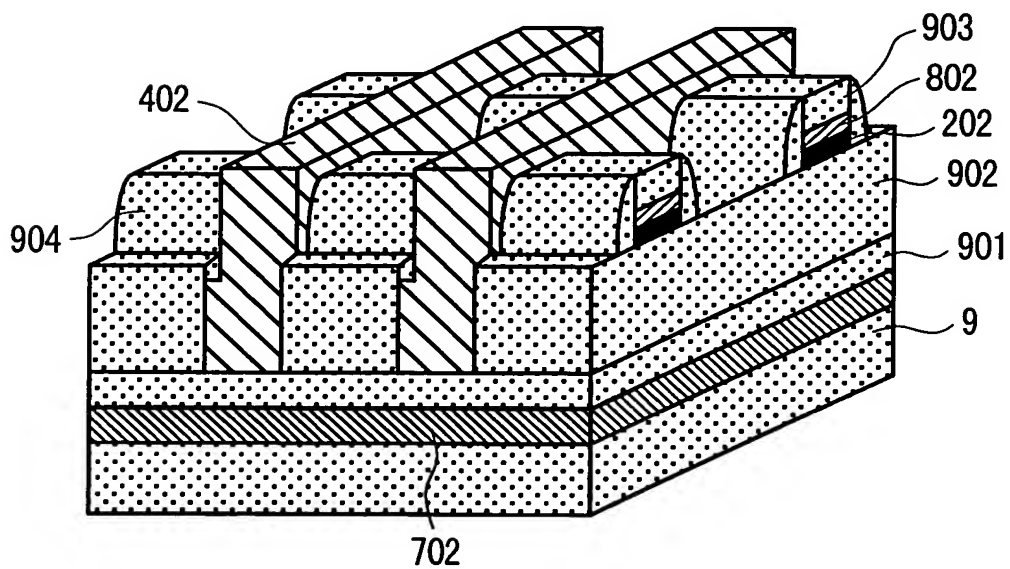


FIG. 16

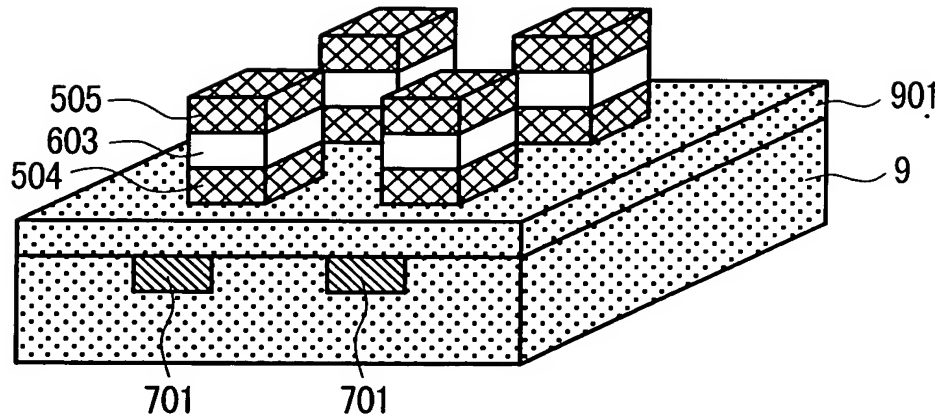


FIG. 17

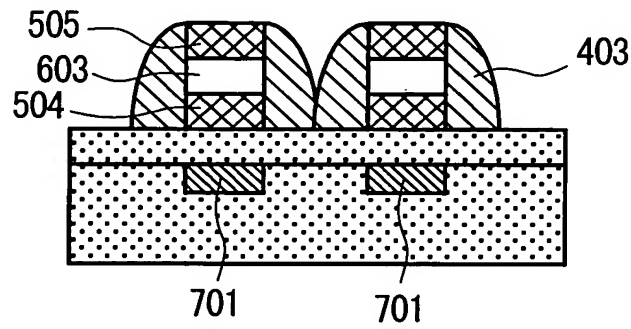


FIG. 18

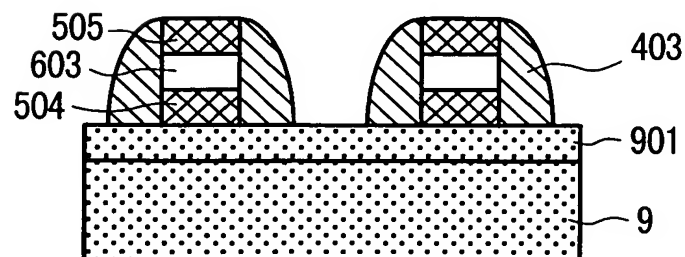


FIG. 19

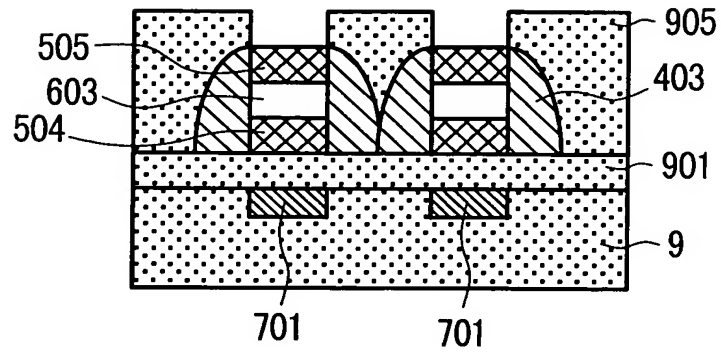


FIG. 20

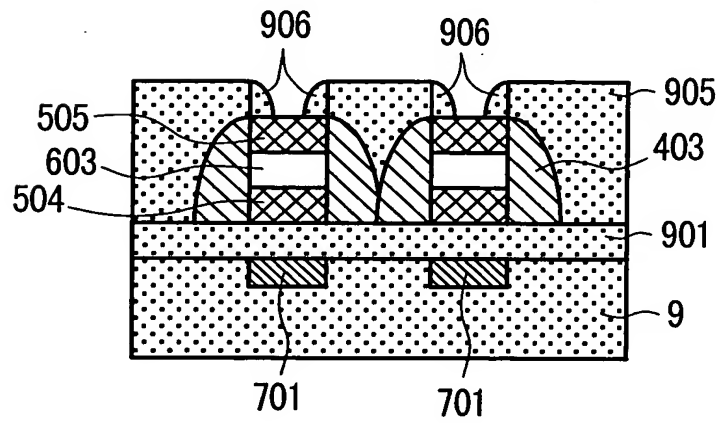


FIG. 21

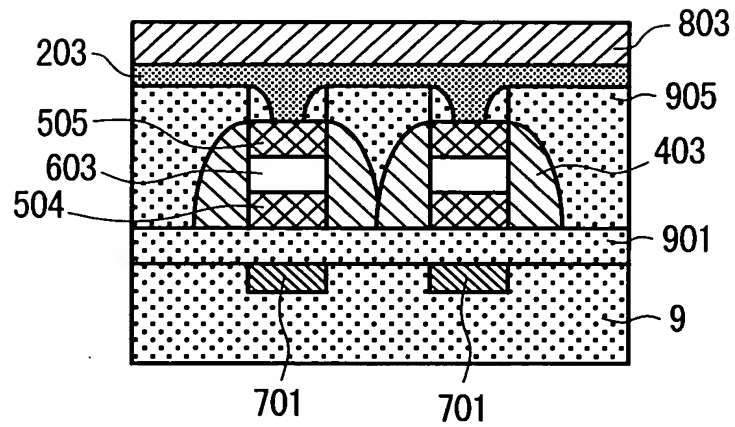


FIG. 22

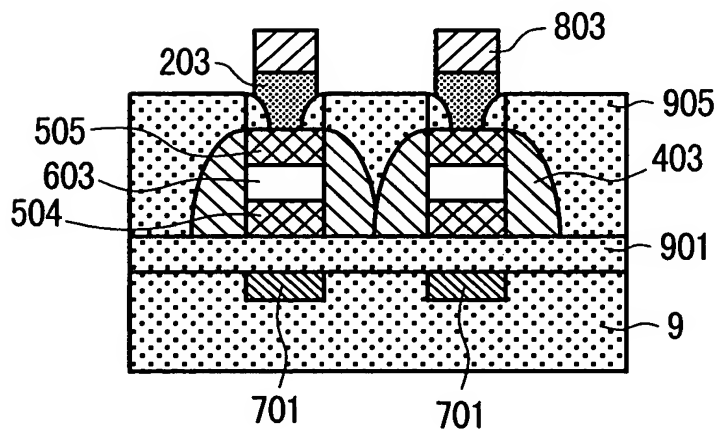


FIG. 23

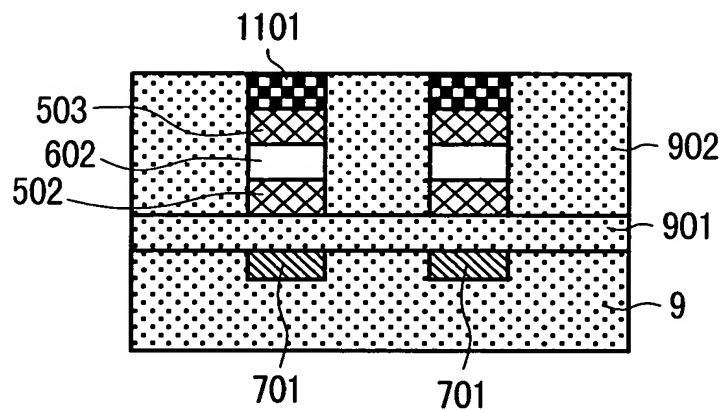


FIG. 24

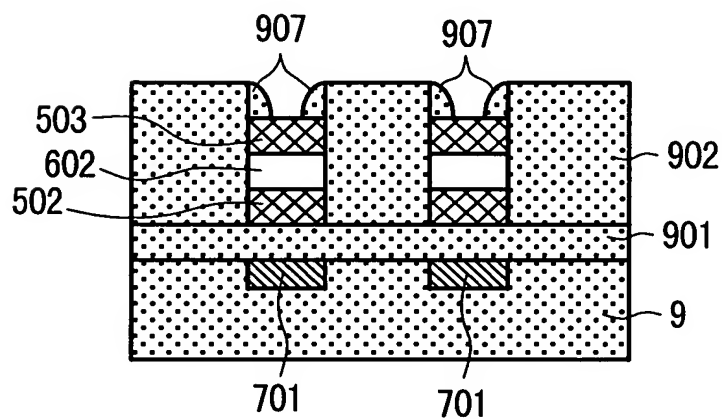


FIG. 25

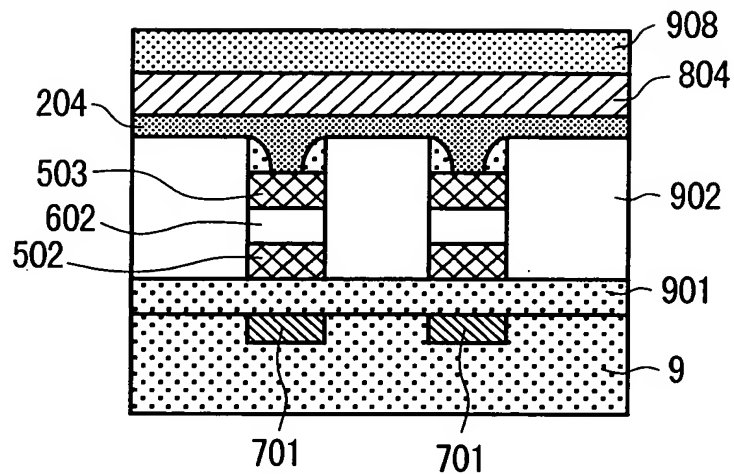


FIG. 26

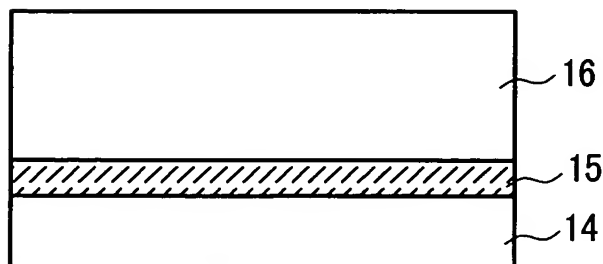


FIG. 27

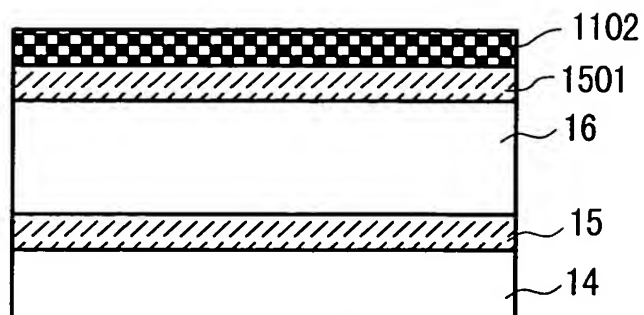
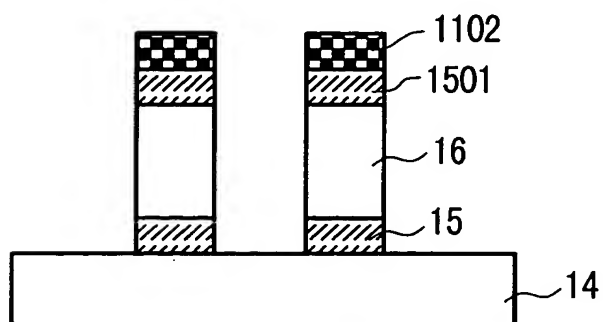


FIG. 28



A cross-sectional view of a semiconductor device 909. The device consists of a substrate 14 at the bottom. Above the substrate is a layer 15, which contains two rectangular openings. The layer 15 is covered by a layer 16, which also contains two rectangular openings. The layer 16 is covered by a layer 1501, which contains two rectangular openings. The layer 1501 is covered by a layer 1102, which contains two rectangular openings. The layer 1102 is covered by a layer 909, which contains two rectangular openings.

This cross-sectional view shows a trench structure within a substrate 14. The trench is filled with a material 15. A layer 16 is deposited on the top surface of the trench. A layer 1501 is formed on the top surface of the layer 16. A layer 909 is formed on the top surface of the layer 1501. A layer 910 is formed on the top surface of the layer 909.

FIG. 31

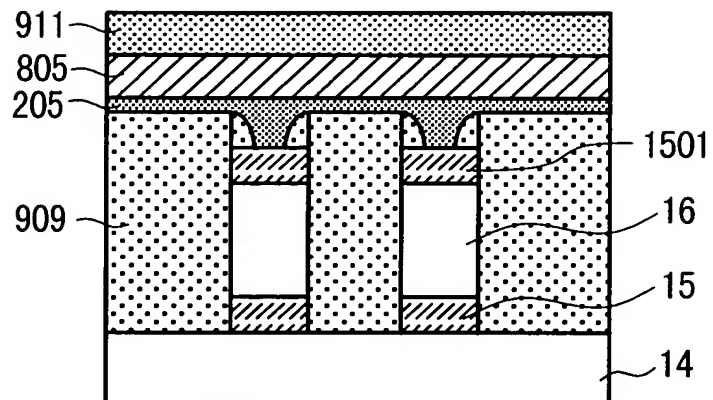


FIG. 32

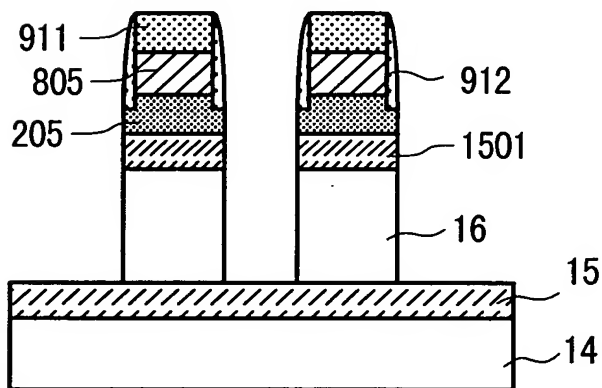


FIG. 33

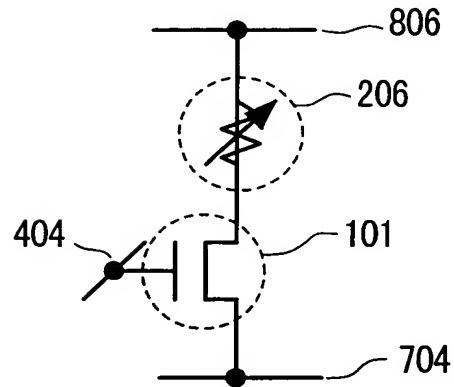


FIG. 34

